

Appl. No. 10/645,366
Amdt. Dated 07/25/2005
Reply to Office Action of 04/26/2005

IN THE SPECIFICATION

Please amend the Specification as follows:

Page 1, line 5, under the CROSS-REFERENCE TO RELATED APPLICATIONS section, please once again amend the paragraph starting there-at from the prior amendment as follows:

"This non-provisional United States (U.S.) patent application claims the benefit of and is a divisional application of U.S. Patent Application No. 10/109,826 filed on March 29, 2002 by inventors Ruban Kanapathippillai, et al., entitled "METHOD AND APPARATUS FOR POWER REDUCTION IN A DIGITAL SIGNAL PROCESSOR INTEGRATED CIRCUIT", now issued as U.S. Pat. No. 6,732,203, which claims the benefit of U.S. Provisional Application No. 60/280,800, filed on April 2, 2001 by inventors Ruban Kanapathippillai et al, entitled "METHOD AND APPARATUS FOR POWER REDUCTION IN A DIGITAL SIGNAL PROCESSOR INTEGRATED CIRCUIT".